



VESP Technology Corporation

Address: Area A, 1st Floor, Building 5, Mingzhu Industrial Park, High-tech Zone, Hefei, Anhui

TEL: +86-21-3880-9688 FAX: +86-21-5080-4105

CDM Test Report

Testing laboratory is accredited by

IEC quality assessment system (IECQ) : ISO/IECQ 17025 (Certificate No. : IECQ-L ULTW 21.0001)

	Signature	Date
Testing Engineer		2022/07/28
Technical Manager		2022/07/28
Approval Manager		2022/07/28



NOTE :

- This report is generated subject to certain conditions (including but not limited to: designated samples, designated environment parameters and designated input signals). VESP Technology Co., Ltd. does not guarantee that the test results under different conditions or generated by other people will coincide with this report.
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- VESP Technology Co., Ltd. is not responsible for whether the samples tested in this report will function well as their original design and/or meet any expectation.

1. Application Information :

1.1 Test Items : Charge Device Model (CDM)

1.2 Test Serial No. : GWO5-22070069

2. Customer Basic Information :

2.1 Company : Hu nan Jin Xin Electronic Technology Co., Ltd.

2.2 Applicant : Song Fan

2.3 Address : 1002-1010 of the headquarters at Changsha CEC Software Park, NO.39
Jian Shan Road, High-tech District, Changsha China

2.4 Package/Pin Count : QFP/176

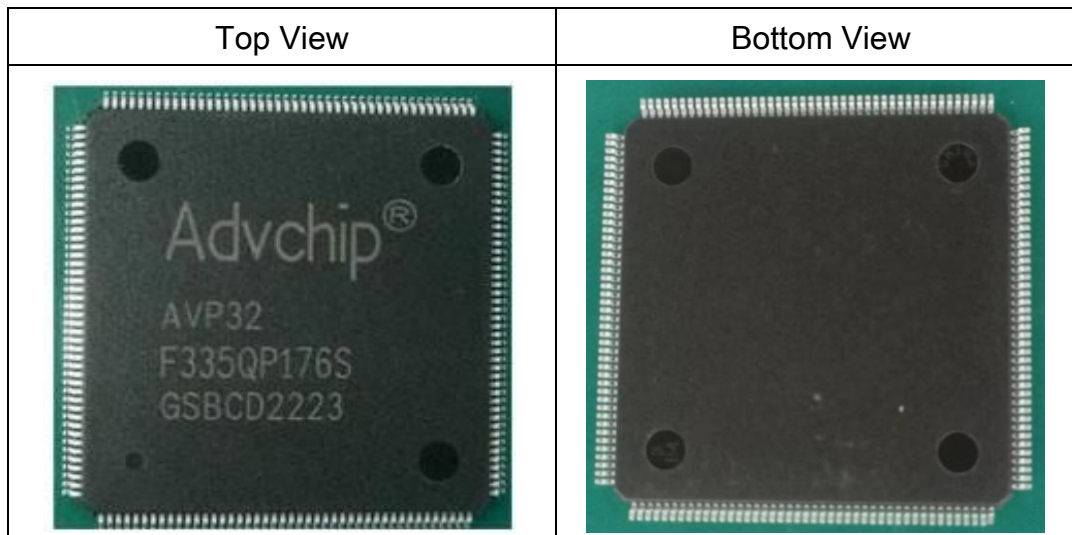
2.5 Application Date : 2022/07/18

2.6 Tested Date : 2022/07/18~2022/07/22

2.7 Device Information :

Device Name	Date Code	Sample Q' ty (ea)
AVP32F335QP176S	GSBCD2223	9

2.8 Device Photos :



3. Environmental Conditions of Laboratory :

3.1 Temperature : 22.7°C

3.2 Humidity : 54.9% R.H.

4. Test Equipment :

4.1 Test Item : Charge Device Mode

4.2 Test Equipment :

Item	Test Equipment	Serial No.	Calibration Expiration
CDM	Thermo Fisher Scientific Orion2	1506254	2022/09/07

5. Test Procedure :

5.1 Reference Specification : JS -002-2018

5.2 Test Procedure and Conditions :

5.2.1 Test Voltage : $\pm 500V$

5.2.2 Pin Assignment :

VDD 3.3V: 4,15,23,29,61,101,109,117,126,139,146,154,167

VDDIO 3.3V: 9,71,93,121,143,159,170

VDDIOPR 3.3V: 107

VDD1A33 3.3V: 84

VDDA2 3.3V: 34

VDDAIO 3.3V: 45

VDD1A18 1.8V: 31

VDD2A18 1.8V: 59

VSS: 3,8,14,22,30,60,70,82,83,92,103,106,108,118,120,125,140,144,147,155,160,
166,171

VSSA2: 33

VSSAIO: 44

VSS1AGND: 32

VSS2AGND: 58

IP 3.3V : 35,36,37,38,39,40,41,42,43,46,47,48,49,50,51,52,53,54,78,87,79,76,105,
104

OP 3.3V: 77,138,102,57,149

IO 3.3V: 1,2,5,6,7,10,11,12,13,16,17,18,19,20,21,24,25,26,27,28,55,56,62,63,64,
65,66,67,68,69,72,73, 74,75,85,86,80,88,89,90,91,94,95,96,97,98,99,100,
110,111,112,113,114,115,116,119,122,124,127,123,128,129,130,131,132,
133,134,135,136, 137, 141, 142,148,145,150,151,152,153,156,157,158,
161,162, 163,164,165,168,169,172,173,174, 175,176

6. Test Result :

Model : CDM	ESD Sensitivity Pass: ±500V		V Class : <u>C1</u>
Pin Combination	Sample Size	Pass Voltage	Note :
ALL PINS	3	500V	Bases on JS -002-2018 CLASS C0A :<125 V CLASS C0B :125 to <250 V CLASS C1 :250 to <750 V CLASS C2 :750 to <1000 V CLASS C3 : 1000 V or greater

7. Special Remarks : NA

**8. Attachment :**Charge Device Model : +500V Modes

(Unit : Volt)

Test Mode		CDM +500V Mode		
Pin	Pass Voltage	#C1	#C2	#C3
	Name			
1		PASS	PASS	PASS
2		PASS	PASS	PASS
3		PASS	PASS	PASS
4		PASS	PASS	PASS
5		PASS	PASS	PASS
6		PASS	PASS	PASS
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8		PASS	PASS	PASS
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172	PASS	PASS	PASS
173	PASS	PASS	PASS
174	PASS	PASS	PASS
175	PASS	PASS	PASS
176	PASS	PASS	PASS

The tested Sample (units) was complied with test level +500V, I-V curve shift $\leq 10\%$.

Charge Device Model : -500V Modes

(Unit : Volt)

Test Mode		CDM:C-N-500V Mode		
Pin	Name	#C4	#C5	#C6
1		PASS	PASS	PASS
2		PASS	PASS	PASS
3		PASS	PASS	PASS
4		PASS	PASS	PASS
5		PASS	PASS	PASS
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175	PASS	PASS	PASS
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The tested Sample (units) was complied with test level -500V, I-V curve shift \leq 10%.